

TSMC 98-338BC

Application no. 09/726,663

April 4, 2003

#12/D

5-21-03
Muller



TO: Commissioner of Patents and Trademarks
Washington, D.C. 20231

FROM: Stephen Ackerman Reg. No. 37,761
28 Davis Avenue
Poughkeepsie, N.Y. 12603

SUBJECT: Serial # 09/726,663
File Date: 11/30/2000
Inventor: Lin, Chrong-Jung
Examiner: Dickey, Thomas L.
Art Unit: 2826
Title: Step-Shaped Floating Poly-Si Gate To Improve Gate
Coupling Ratio For Flash Memory Application

RECEIVED
MAY 14 2003
TECHNOLOGY CENTER 2826

RESPONSE TO OFFICE ACTION

This is in response to the Office Action dated 02/06/2003 Please amend
the above identified application for patent as follows:

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United
States Postal Service as first class mail in an envelope addressed to
Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

May 6, 2003.

Signature [Signature] Date: May 6, 2003
Stephen B. Ackerman, Reg. No. 37,761